



**MAULANA ABUL KALAM AZAD UNIVERSITY OF TECHNOLOGY.
WEST BENGAL**

**ROUTINE FOR 1ST & 3RD SEM M.TECH ECE (VLSI) (NEW) 2018 - 19
COLLEGE-104/105/130/209**

DATE	TIME	P CODE	PAPER NAME
13/12/2018	2.00PM-5.00PM	MVLSI-101	Advanced Engineering Mathematics
14/12/2018	2.00PM-5.00PM	MVLSI-102	VLSI Device & Modelling
17/12/2018	2.00PM-5.00PM	MVLSI-103	Digital IC Design
19/12/2018	2.00PM-5.00PM	MVLSI-104	Microelectronic Technology & IC Fabrication
21/12/2018	2.00PM-5.00PM	ELECTIVE	MVLSI-105A/B/C/D/E

COLLEGE-103/104/105/130/209

DATE	TIME	P CODE	PAPER NAME
13/12/2018	10.00AM-1.00PM	MVLSI-302A	Architectural Design of Integrated Circuits
13/12/2018	10.00AM-1.00PM	MVLSI-302B	Low Power VLSI Design
13/12/2018	10.00AM-1.00PM	MVLSI-302C	Advanced VLSI Design
13/12/2018	10.00AM-1.00PM	MVLSI-302D	Advanced Analog & Digital VLSI Circuit & System
13/12/2018	10.00AM-1.00PM	MVLSI-302E	Asic Design for DSP Applications
14/12/2018	10.00AM-1.00PM	MVLSI-381A	Project management
14/12/2018	10.00AM-1.00PM	MVLSI-381B	Teaching & Research Methodologies

Elective list

PAPER CODE	PAPER NAME
MVLSI-105A	Bioelectronic System
MVLSI-105B	Embedded System Fundamentals
MVLSI-105C	AI & Neural Networks
MVLSI-105D	Advanced Digital Communication

**Sd/-
Haider Ali
Controller of Examinations (OSD)**