One Week
ISTE-GTU Sponsored
Faculty Development
Programme
on
“Research
Opportunities in VLSI
and Signal Processing”
Date: 11/02/2019 to
16/02/2019

Objective: There are significant developments in the field of VLSI and Signal Processing.

Tremendous growth in the field of VLSI is contributed by the continuous down-scaling of MOSFET device. Channel length of MOSFET device is scaled down to below 20 nm. It has resulted into significant improvement in the performance of MOSFET based circuits in terms of speed, low power technology, portability and compactness, introduction of advanced features in electronic systems, etc. The down scaling of MOSFET technology is complemented by accurate and compact MOSFET model development, novel and efficient circuit design techniques, efficient CAD tools, fabrication technology, investigation and use of new materials, interconnect technology, etc.

Signal processing has remained a very interesting and wide domain of research since quite long. Researchers continuously come out with efficient techniques for various signal processing application such as Estimation, Filtering, Image processing, Speech processing, Video processing, etc.

This one week research oriented faculty development programme on “Research opportunities in VLSI and Signal Processing” is aimed to provide exposure to interested candidates willing to pursue their career in these domains.

This FDP is mainly designed for the interested faculties from EC and other related allied braches to provide exposure to recent development in the field of VLSI and Signal processing. This programme will provide a window to current research problems in the field of VLSI and Signal processing.

Eligibility: Faculty members of Electronics and communication, Computer and other allied disciplines of Degree/ Diploma Engineering Institutes are invited to attend this programme.

Course Fee: There is a fee of 500/- Rs. for the faculty of Degree/Diploma Engineering Institutes. Participant must become a member of ISTE at the time of registration, if not. Number of participants is limited to 40. Participants are requested to manage accommodation. Please send a Demand Draft in favor of “Principal, VGEC Chandkheda.”
Registration Link:
https://docs.google.com/forms/d/e/1FAIpQLSfUKoFB227a8MeMuPJp9y7c3o33WI8ZTSfR20z3iE3NW-iw/viewform

About VGEC – Chandkheda:
Vishwakarma Government Engineering College – Chandkheda is established in the year 1994. It offers degree engineering courses in nine different disciplines. Departments of Electronics and Communication, and Chemical Engineering are offering two years PG Programme. The specialization offered by EC Department for PG course is Signal Processing and VLSI Technology. This Institute is considered among few best Engineering Institutes of the state. More than 125 Electronics Engineers are passing out every year from this Institute.

This Institute is located in Chandkheda which is highly developing area. The campus is presently shared with IIT Gandhinagar and GTU.

Speakers: Experts from academic and research Institutes such as various IITs and NITs, DAIICT, Nirma, SAC, IPR, etc. will be sharing their valuable knowledge with the participants during this one week programme. Experts from Industries will also be invited to share their knowledge and practical experience with the participants. The hands-on session will provide exposure to practical implementation.

Application Form

One Week FDP on “Research Opportunities in VLSI and Signal Processing”

Duration: 11/02/2019 to 16/02/2019

Name: ____________________________

Designation: _______________________________

Highest Qualification: _______________________________

Specialization: _______________________________

Institute’s Name: _______________________________

Address: _______________________________

Experience:

1. Teaching
2. Industry:

Contact Details:

1. Phone No.: _______________________________
2. Email Id: _______________________________

Demand Draft Details:

Number: _______________________________

Date: _______________________________

Amount: _______________________________

Drawn on: _______________________________

Sign of the participant

Certificate

This is to certify that Mr./Ms./Mrs.______________________________is working as a/an _________ in __________________________ Department of this Institute. He/She will be permitted to attend this one week oriented training programme, if selected.

Authorized signatory

(Principal of the sponsoring Institute)

Stamp and Seal of the Institute

Principal: Dr. R. K. Gajjar

Coordinators:

1. Dr. R. A. Thakker, hod_ec@vgecg.ac.in, M: 9426333130, Professor – EC Dept., VGEC – Chandkheda

2. Dr. Milind Shah, (mssvgec@gmail.com) M: 7874563242, Associate Professor – EC, VGEC – Chandkheda