



GUJARAT TECHNOLOGICAL UNIVERSITY

(Established Under Gujarat Act. No.:20 of 2007)

Date: 21-05-2018

CIRCULAR

Interested faculty members and students may register for the following webinar which is going to be held on Tue, May 22, 2018 3:30 PM - 4:30 PM IST.

ICTIEE 18 Paper: "Redesign of Digital Circuits course for enhanced learning: An experience"

Tue, May 22, 2018 3:30 PM - 4:30 PM IST

Registration URL: <https://attendee.gotowebinar.com/register/>

Description:

The paper presents the restructuring of course design and delivery, to attain the enhanced learning in Digital Circuits course through pedagogic practices and hands on experience in laboratory. The objective of the paper is to enhance the understanding of the course beyond the class room teaching. The traditional approach of course design, course delivery and course assessment provide less scope for better learning of courses. To address the limitations of traditional approaches, an Outcome Based Education (OBE) proposes many pedagogical practices. The paper proposes a framework to restructure the Digital Circuits course w.r.t. to content delivery in the class, course projects and the reframing the laboratory experiments. The contents delivery is supported by expert lecture through videos, course project is aided by concept/prototype development and the lab is formulated to exercise multiple problems on a single topic. The students' attainment is measured and analyzed through examination results and feedback. The technical and professional outcomes are achieved through the proposed framework.

Presenter:

Prof. Suneeta V. Budihal
Associate Professor,
Dept. of ECE
BVBCET-Hubli

Sd/-
Registrar (I/c)