

GUJARAT TECHNOLOGICAL UNIVERSITY
M.E.in VLSI System Design
Semester: 1

Subject Name: **HDL Based Design & Simulation Lab**
Subject Code: **714207**

The laboratory work consists of exercise/ assignments based on designing of adders, subtractors, multiplexers, ALU, memory and other digital circuits. The exercise involves designing of circuits, writing of VHDL codes and implementation on FPGA/CPLD boards. HDL Simulation and Synthesis tools are to be used.