

GUJARAT TECHNOLOGICAL UNIVERSITY

Master of Engineering (VLSI System Design Engineering)

Semester – II

Subject Code: 1724207

Subject Name: Hardware Software Co-Design (Institute Elective –II)

Sr. No.	Course Content
1	Introduction
2	Models of computation for embedded systems
3	Behavioral design, Architecture selection
4	Partitioning, scheduling, and communication, Simulation, synthesis, and verification,
5	Hardware/Software Implementation,
6	Performance analysis and optimization
7	Design methodologies and tools, Design examples and case studies

Textbooks:

1. Specification and Design of Embedded Systems, D. Gajski, F. Vahid, S. Narayan, and J. Gong, Prentice Hall, 1994.
2. System C Specification Language, Kluwer, 2000
3. Hardware Software Co-design of Embedded Systems, F. Balarin, Chiodo, et al., Kluwer Academic Publishers, May 1997 (ISBN 0-7923-9936-6).