

INDIAN INSTITUTE OF TECHNOLOGY, ROORKEE
Electronics & Communication Engineering Department

Dated: 1.10.2020

ADVERTISEMENT TO FILL UP PROJECT POSITIONS*

Applications are invited from Indian nationals only for project position(s) as per the details given below for the consultancy/research project(s) under the Principal investigator (Name: **Dr. Anand Bulusu**), Dept./Centre **Electronics & Communication Engineering**, Indian Institute of Technology, Roorkee.

1. Title of project: : A PDSOI Analog cell library consisting 2 stage OPAMPs and comparators designed considering floating body (FB) and self heating (SH) Effects.
 2. Project position(s) and number : SRF (No. One)
 1. Qualifications : M.Tech in Microelectronics or equivalent with 4 years experience/ Pre synopsis completed PhD will be preferred
 2. Emoluments : 35000/-
 3. Duration : One year extendable to two years
 4. Job description : Transistor Level Circuit Design of Analog CMOS Circuits, Semiconductor Device Physics, TCAD process/device simulations. Layout and parasitic extraction, SPICE.
 1. Candidates before appearing for the interview shall ensure that they are eligible for the position they intend to apply.
 2. Candidates desiring to appear for the Interview should submit their applications with the following documents to the office of Principal Investigator through email to vlsi-office@ece.iitr.ac.in , by post or produce at the time of Interview:
 - Application in a plain paper with detailed CV including chronological discipline of degree/certificates obtained.
 - Experience including research, industrial field and others.
 - Attested copies of degree/certificate and experience certificate.
 3. Candidate shall bring along with them the original degree(s)/certificate(s) and experience certificate(s) at the time of interview for verification.
 4. Preference will be given to SC/ST candidates on equal qualifications and experience.
 5. Please note that no TA/DA is admissible for attending the interview.
- The last date for application to be submitted to office of Principal Investigator through Email (vlsi-office@ece.iitr.ac.in) is 15.10.20 by 5 PM.
(not applicable for walk in interview)

The interview will be held at **ECE, IITR** on **20.10.2020** at **10.30 A.M.** (to be given only for walk in interview) or would be conducted in online mode. The mode will be notified later.

Tel: 01332-285347 Fax:

*B. Anand
(Anand Bulusu)*
Name and signature
of Principal Investigator

Email: anandfec@iitr.ac.in

*To be uploaded on IIT Roorkee website and copy may be sent to appropriate addresses by PI for wider circulation.

Approved

Manish
कुलशासक (सि.रि.क.)/Dean (SRIC)
Rajeev
1/10-2020