



## Advertisement of a JRF position

### **Indian Institute of Technology, Ropar (IIT Ropar) Department of Electrical Engineering, J C Bose Block, IIT Ropar, Rupnagar, Punjab 140001**

Applications are invited for a junior research fellow (JRF) position in the Department of Electrical Engineering, IIT Ropar under the SERB sponsored project titled “**Design and Development of Single Chip Gas Sensor Arrays Based on Two-Dimensional MoS<sub>2</sub> and Metal Oxide Hybrid Nanomaterials for Air Pollutants Monitoring at Near Room Temperature**”. The overall objective of the project is to design, fabricate, and characterize the gas sensor device using standard fabrication processing methods.

**Essential qualifications:** Master's degree in Electrical Engineering/ Electronics and Communication Eng./ VLSI/ Electronics Eng./ Electronics and Instrument Eng/Equivalent branches. or a Master's degree by Research in VLSI/Electronics and Communication Eng./ Electronics Eng./ Electronics and Instrument Eng/Equivalent Engineering or Technology branches with a GATE or CSIR UGC NET qualified score. Candidates must have obtained at least 60% marks (or 6.5 CGPA) (55% marks or 6.0 CGPA for SC/ST) in their Master's. Deserving candidates will be considered for Ph.D. admissions at a later stage if they meet all the institute requirements.

**Desired qualifications:** Experience in semiconductor devices/ fabrication technology will be an added advantage.

**Duration:** The position is for two years in total. However, it will be given initially for one year, which can be extended for a further one year based on the evaluation of the performance by the principal investigator.

**Salary:** A consolidated salary of **Rs. 31000 / month** will be provided as per the DST norms.

The applicant must send the following documents in a single PDF file to **r.brajesh@iitrpr.ac.in (Dr. Brajesh Rawat)** on/before **08th December 2020**.

1. A one-page cover letter describing the background and how it will help to pursue this project.
2. Resume with complete qualification and experience details.
3. Soft copy of all degree certificates.
4. Proof of GATE/NET qualification.

**The selected candidates will be called for an online interview on 9th December '2020. Please note that no TA/DA will be provided.**

For any further information, please contact the principal investigator:

Dr. Brajesh Rawat  
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